

Amendments to the Claims

1. (currently amended) A photodetector array comprising a plurality of addressable active pixels, each pixel comprising:

at least two photodiodes arranged such that their outputs ~~may be~~ are switchably connected to a common pixel node;

a switching circuit which allows switching of at least one of said photodiodes between a first circuit and a second circuit;

wherein said first circuit directly combines the outputs of said at least two photodiodes in parallel, and said second circuit directly combines the output of said at least one of said photodiodes in parallel with the output of a photodiode of a neighboring pixel in the array, whereby said array is switchable between a high resolution and a low resolution pixel configuration, ~~said~~ each pixel having an intrinsic capacitance which stores said combined photodiode outputs prior to their being read out, and

an addressing circuit which enables the combined photodiode outputs stored on said pixel's intrinsic capacitance to be read out in response to an address input.

2. (original) The photodetector array of claim 1, wherein said switching circuit includes active semiconductor switching devices.

3. (original) The photodetector array of claim 2, wherein said switching devices are field effect transistors.

4. (canceled)

5. (original) The photodetector array of claim 1, wherein said at least two photodiodes consist of two photodiodes.

6. (canceled)

7. (canceled)

8. (canceled)

9. (canceled)

10. (canceled)

11. (canceled)

12. (canceled)

13. (currently amended) The photodetector array of claim ~~15~~1, wherein said array is switchable between 1920 rows and 1080 rows.

14. (original) The photodetector array of claim 13, wherein said array is switchable between 1080 rows and 720 columns.

15. (canceled)